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TITLE

METHOD FOR FABRICATING A TITANIUM NITRIDE SENSING MEMBRANE ON AN EGFET

BACKGROUND OF THE INVENTION

5 Field of the Invention

The present invention relates to a sensing membrane on an ion sensitive field effect transistor (ISFET), and in particular to a method for fabricating a titanium nitride (TiN) sensing membrane on an extended gate field effect
10 transistor (EGFET). The method forms the TiN sensing membrane as an ion sensitive sensor (pH sensor) using a radio frequency (RF) sputtering process and a standard complementary metal oxide semiconductor (CMOS) process. The method uses the standard CMOS process for mass production
15 providing the benefits of low cost, high yield, and high performance.

Description of the Related Art

A glass electrode has typically been used as an ion sensing electrode. A glass electrode has the disadvantages
20 of difficulty in measuring miniaturized structures, susceptibility to damage, and low portability. To solve those problems, Piet Bergveld introduced an ion sensitive field effect transistor (ISFET) in 1970. ISFET has the advantages of compatibility with standard CMOS process, high
25 input impedance, and low output impedance, miniaturization capability minute solution measurement, rapid response, and pH measurement capability. Therefore, ISFET has been

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developed and applied in a variety of fields relating to miniaturized pH sensors and biotechnical sensing devices.

In the 1970s, research on ISFET pH sensors and related applications had just begun. In the 1980s, research on
5 ISFET pH sensors had progressed to a more advanced level regarding aspects of fundamental theories, crucial techniques, and practical applications. Over 30 different ISFETs have been introduced to measure several kinds of ion and chemical materials. Great strides have also been made
10 in miniaturization, modulization, and multi-functionality. According to current research, a hydrogen ion sensing membrane on a gate oxide layer of an ISFET typically comprises silicon dioxide (SiO₂), silicon nitride (Si₃N₄), tantalum oxide (Ta₂O₅), aluminum oxide (Al₂O₃), or other
15 materials. These materials have the disadvantages of low linear sensitivity and low photostability, particularly after long-term operation. Additional processes are also required to handle these materials. Due to the described problems, a replacement material is called for.

20 **SUMMARY OF THE INVENTION**

Accordingly, an object of the present invention is to provide a method for fabricating a titanium nitride (TiN) sensing membrane on an extended gate field effect transistor (EGFET). The method forms the TiN sensing membrane as an
25 ion sensitive sensor (pH sensor) using a radio frequency (RF) sputtering process and a standard CMOS process. The method uses standard CMOS process for mass production and provides the benefits of low cost, high yield, and high performance.

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The TiN sensing membrane has the features of long-term stability with low drift, rapid response time of less than 0.1 second, and high linear sensitivity of approximately 56 to 58 mV/pH due to the Nernst response. The method of the present invention comprises the steps of depositing a layer of aluminum on a gate terminal of the EGFET using thermal evaporation, wherein the layer of aluminum extends from the gate terminal to a sensitive window of the EGFET and forms the TiN sensing membrane on an exposed part of the aluminum layer in the sensitive window as an ion sensitive sensor (pH sensor) using a radio frequency (RF) sputtering process during which TiN is used as a sputtering target and a mixture of argon and nitrogen with a ratio of 9:1 is used as a reactant. A substrate temperature of 150°C, a deposition pressure of 5 milli-torrs, a sputtering duration of 1 hour, and an RF power of 90 watts are suited to an operating condition for forming the TiN sensing membrane. The TiN sensing membrane has a thickness of about 2000Å.

A detailed description is given in the following embodiments with reference to the accompanying drawings.

BRIEF DESCRIPTION OF THE DRAWINGS

The present invention can be more fully understood by reading the subsequent detailed description and examples with references made to the accompanying drawings, wherein:

Fig. 1 is a schematic diagram showing an EGFET circuit of the present invention.

Fig. 2 is a flowchart showing a process of fabricating a TiN sensing membrane of the present invention.

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Fig. 3 is a cross-section showing a laminated structure of the EGFET circuit in Fig. 1 of the present invention.

Fig. 4 is a schematic cross-section showing a laminated structure of the EGFET circuit in Fig. 1 of the present
5 invention.

Fig. 5 is a schematic diagram showing a measurement configuration of the present invention.

Fig. 6 is a plot showing curves of a sensing current to a reference voltage ($I_{DS}-V_{REF}$) under different pH buffer
10 solutions for the EGFET pH sensor in Fig. 5 of the present invention.

Fig. 7 is a plot showing curves of a sensing current to a sensing voltage ($I_{DS}-V_{DS}$) under different pH buffer solutions for the EGFET pH sensor in Fig. 5 of the present
15 invention.

Fig. 8 is a schematic diagram of amplifier meters used as a readout circuit of the present invention.

Fig. 9 is a plot showing curves of an output voltage of a pH sensor in Fig. 8 with respect to duration of the
20 present invention.

Fig. 10 is a plot showing curves of an output voltage of a pH sensor in Fig. 8 with respect to pH values ($V_{OUT}-pH$) of the present invention.

Fig. 11 is a plot showing curves of an output voltage
25 of a pH sensor in Fig. 8 with respect to the duration of hysteresis voltage testing of the present invention.

Fig. 12 is a schematic cross-section showing a laminated structure of an EGFET circuit with a temperature sensor and a photosensor of the present invention.

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DETAILED DESCRIPTION OF THE INVENTION

Fig. 1 is a schematic diagram showing an EGFET circuit of the present invention. As shown in Fig. 1, a sensing structure including a sensing window 110 and a sensing membrane 111 is immersed in a pH buffer solution to form an electrically conductible circuit by which the pH value sensed by the sensing membrane 111 is output through an output end OUT to an extra readout circuit described hereinafter.

Fig. 2 is a flowchart showing a process of fabricating a TiN sensing membrane of the present invention. As shown in Fig. 2, the method of the present invention comprises the steps of depositing a layer of aluminum on a gate terminal of the EGFET using thermal evaporation, wherein the layer of aluminum extends from the gate terminal to a sensitive window of the EGFET and forming the TiN sensing membrane on an exposed part of the layer of aluminum in the sensitive window as an ion sensitive sensor (pH sensor) using a radio frequency (RF) sputtering process during which TiN is used as a sputtering target and a mixture of argon and nitrogen in the ratio of 9:1 is used as a reactant. A substrate temperature of 150°C, a deposition pressure of 5 millitorrs, sputtering duration of 1 hour, and an RF power of 90 watts are the preferred operating conditions for forming the TiN sensing membrane. The TiN sensing membrane has a thickness of about 2000Å. The TiN sensing membrane has features of long-term stability with low drift, rapid response time of less than 0.1 seconds, and high linear sensitivity of approximately 56 58 mV/pH due to the Nernst

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response. A detailed description of the EGFET is provided in the following.

Fig. 3 is a cross-section showing a laminated structure of the EGFET circuit in Fig. 1 of the present invention. As shown in Fig. 3, an EGFET consists essentially of a sensing structure and an N-type metal oxide semiconductor field effect transistor (MOSFET), wherein the sensing structure is the same as previously described and the N-type MOSFET is described in the following.

Fig. 4 is a schematic cross-section showing a laminated structure of the EGFET circuit in Fig. 1 of the present invention. Fig. 4 shows cross-sectional layers fabricated by the standard 0.5 μ m CMOS integrated circuit (IC), double poly double metal (2P2M), N-type well process. In Fig. 4, in addition to the deposition layers and metal layers produced by the standard CMOS IC process, an N-type diffusion region 22 is also produced by the doping step of the standard process to form the MOSFET structure in Fig. 3. As shown in Fig. 4, the layers included a passivation layer 10 with a 0.7 μ m thickness, a metal layer 11 with a 1.1 μ m thickness, an oxide layer 12 with a 0.5 μ m thickness, a metal layer 13 with a 0.6 μ m thickness, an oxide layer 14 with a 0.5 μ m thickness, a gate poly-silicon layer 15 with a 0.3 μ m thickness, a gate oxide layer 16 with a 135 \AA thickness, a capacitance poly-silicon layer 17 with a 0.2 μ m thickness, a field effect oxide layer 18 with a 0.5 μ m thickness, an N-type metal oxide semiconductor (NMOS) included in an EGFET, disposed over a P-type silicon substrate 19, and having a width to length (W/L) ratio of 600 μ m/20 μ m, a 300 μ m X 300 μ m

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sensitive window, and a chip with a total area of 1.8mm X 1.8mm.

As shown in Fig. 3, the chip includes an N-type MOSFET (NMOSFET), formed by an N-type diffusion region 221 (applying the N-type diffusion region 22 of Fig. 4) and a middle P-type substrate 190 (applying the P-type silicon substrate 19 of Fig. 4), with an extended aluminum structure 130 (applying the metal layer 13 of Fig. 4), using the standard IC process. The layer of aluminum membrane 130 disposed on a gate terminal 150 (applying the gate polysilicon layer 15 of Fig. 4) extends from the gate terminal to a sensitive window 110 of the EGFET. With the exception of the area directly above the sensitive window 110, the aluminum layer 130 is coated with an insulator, a layer of glass A, to achieve the required NMOSFET structure. Moreover, a TiN sensing membrane 110 (applying the metal layer 11 of Fig. 4) is formed on the sensitive window 110 to produce an EGFET-based chip 30 of the present invention using a sputtering process. The sensitivity of the completed EGFET component is measured to be 56.3 mV/pH, very close to the ideal Nernst response, indicating a highly linear relationship curve between the sensing voltage level and the output voltage. The NMOSFET is the preferred MOSFET structure of the present invention as it has a higher mobility than a PMOSFET.

Fig. 5 is a schematic diagram showing a measurement configuration of the present invention. As shown in Fig. 5, the chip 30 on a ceramic base 51 is encapsulated with epoxy resin (epoxy) to provide electrical insulation for the bonding wires and exposed silicon regions of the chip 30.

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The encapsulated chip 54 and a reference electrode 55 are immersed in a buffer solution 52 for 1 minute in order to measure and analyze the sensors of the chip 30 through the sensitive window 110 with the TiN sensing membrane 111. In practice, the HP4145B semiconductor parameter analyzer 53 was used to obtain sensor output through the integrated readout circuit via the TiN sensing membrane 111 and refers to the voltage V_{REF} of the reference electrode 55 to generate corresponding output V_{OUT} .

Fig. 6 is a plot showing curves of a sensing current to a reference voltage ($I_{DS}-V_{REF}$) under different pH buffer solutions for the EGFET pH sensor in Fig. 5 of the present invention. In Fig. 6, the lateral axis is the reference voltage V_{REF} of reference electrode 55 and the vertical axis is the sensing current I_{DS} . When a Ag/AgCl-based reference electrode 55 is immersed in the buffer solutions 52 with pH values from 1 to 12 for 1 minute at room temperature and then the measurement is performed under operating conditions, a family of curves can be generated by applying the drain-to-source voltage $V_{DS}=0.2V$ for the reference voltage V_{REF} . As shown in Fig. 6, the resulting pH values of the solution 92 and the reference voltage V_{REF} are inversely proportional to the current I_{DS} , so the measured current I_{DS} goes higher as the pH value becomes smaller. Therefore, the sensing purpose is achieved using the transfer characteristics of the sensing current I_{DS} of which the output value varies significantly according to the pH values of different buffer solutions.

Fig. 7 is a plot showing curves of a sensing current to a sensing voltage ($I_{DS}-V_{DS}$) under different pH buffer

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solutions for the EGFET pH sensor in Fig. 5 of the present invention. As shown in Fig. 7, when a pH sensor is immersed in different pH buffer solutions, the variations between the drain-to-source current I_{DS} and voltage V_{DS} under different pH buffer solutions is detected and analyzed. The result shows that the drain-to-source current varies with the turn-on voltage of the pH buffer solution. When applying the reference electrode voltage $V_{REF} = 2.0V$ in operation, the drain-to-source voltage V_{DS} fluctuates from 0 to 5V and thus the device is functional in a saturation region. As shown in Fig. 7, the current I_{DS} varies as a function of square root.

Fig. 8 is a schematic diagram of amplifier meters used as a readout circuit of the present invention. As shown in Fig. 8, the amplifier meters read the voltage level difference between that of the sensing membrane and that of the reference electrode, and the difference varies according to a pH value in the range of 1 to 12. The initial gain is 2.

Fig. 9 is a plot showing curves of an output voltage of a pH sensor in Fig. 8 with respect to time in the present invention. In Fig. 9 the lateral axis is the time t and the vertical axis is the output voltage V_{OUT} . Within the measured time of 1 minute, the value of the output voltage V_{OUT} is read for each pH value increasing from 1 to 12 by an increment of 1. As shown in Fig. 9, each value of the output voltage V_{OUT} roughly remains on a fixed value.

Fig. 10 is a plot showing curves of an output voltage of a pH sensor in Fig. 8 with respect to pH values (V_{OUT} -pH) of the present invention. As shown in Fig. 10, the

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sensitivity is almost linear and the value is 56.3 mV/pH. This result fulfills the expectation that the TiN sensing membrane has a linear relationship with the output voltage, and high linear sensitivity due to the Nernst response.

5 Fig. 11 is a plot showing curves of an output voltage of a pH sensor in Fig. 8 with respect to time for testing a hysteresis voltage of the present invention. As shown in Fig. 11, a sensing component was immersed in different pH buffer solutions in the sequence of pH = 7 → 4 → 7 → 10 →
10 7. Each immersion period was 2 minutes. The resulting hysteresis voltage is determined to be 0.5mV by subtracting the value of the initial voltage from that of the final voltage. This result shows the sensing component has the feature of high reusability.

15 Fig. 12 is a schematic cross-section showing a laminated structure of an EGFET circuit with a temperature sensor and a photosensor of the present invention. As shown in Fig. 12, the TiN sensing membrane can be used in structures comprising single or multiple sensing components.
20 Multiple sensing components including pH sensors, temperature sensors, and photosensors are integrated on a single chip using the standard process for fabricating silicon chips. The structure of the pH sensor is the same as previously described with reference to Figs. 3 and 4.
25 The temperature sensor is fabricated by forming an N-type well 20 over a P-type substrate 19 and then a P-type diffusion region (p⁺) 21 within the N-type well 20 using an ion implantation process to form a p⁺-N diode to sense a temperature by determining a decreased turn-on voltage to be
30 a higher temperature under a forward bias. The photosensor

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is fabricated by forming an N-type diffusion region (n') 23
within the P-type substrate 19 using an ion implantation
process to form a n'-P diode to sense a photo intensity
using the feature that a current caused by changing charge
5 varies with the degree of the photo intensity under a
reversed bias. As cited, all sensors including the pH
sensor, the temperature sensor, and the photosensor as well
as the corresponding readout circuits are fabricated on a
single chip using the standard CMOS IC process, and the
10 entire area used by the chip is only about 1.8 mm X 1.8 mm
including wiring points. Therefore, the chip is suitable
for mass production due to the features of low cost and
limited physical size. Additionally, the integrated chip
can compensate for temperature and optical effects and
15 outputs the desired accurate pH values.

The invention can also be applied to other fields. For
example, the described readout circuit can convert a
biochemical signal or a physical signal to an electrical
signal to be output for measuring the pH value of
20 environmental water, practical for pollution monitors needed
by the industry and pH reaction controls needed by the
bioindustry.

While the invention has been described by way of
example and in terms of the preferred embodiments, it is to
25 be understood that the invention is not limited to the
disclosed embodiments. To the contrary, it is intended to
cover various modifications and similar arrangements (as
would be apparent to those skilled in the art). Therefore,
the scope of the appended claims should be accorded the

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broadest interpretation so as to encompass all such
modifications and similar arrangements.